

August 1986 Revised April 2000

DM74LS155 • DM74LS156 Dual 2-Line to 4-Line Decoders/Demultiplexers

General Description

These TTL circuits feature dual 1-line-to-4-line demultiplexers with individual strobes and common binary-address inputs in a single 16-pin package. When both sections are enabled by the strobes, the common address inputs sequentially select and route associated input data to the appropriate output of each section. The individual strobes permit activating or inhibiting each of the 4-bit sections as desired. Data applied to input C1 is inverted at its outputs and data applied at C2 is true through its outputs. The inverter following the C1 data input permits use as a 3-to-8-line decoder, or 1-to-8-line demultiplexer, without external gating. Input clamping diodes are provided on these circuits to minimize transmission-line effects and simplify system design.

Features

■ Applications:

Dual 2-to-4-line decoder

Dual 1-to-4-line demultiplexer

3-to-8-line decoder

1-to-8-line demultiplexer

- Individual strobes simplify cascading for decoding or demultiplexing larger words
- Input clamping diodes simplify system design
- Choice of outputs:

Totem-pole (DM74LS155)

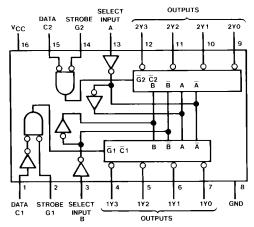
Open-collector (DM74LS156)

Ordering Code:

Order Number	Package Number	Package Description
DM74LS155M	M16A	16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow
DM74LS155N	N16E	16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide
DM74LS156M	M16A	16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow
DM74LS156N	N16E	16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide

Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code

Connection Diagram



Function Tables

3-Line-to-8-Line Decoder or 1-Line-to-8-Line Demultiplexer

Inputs							Out	puts			
Selec	et		Strobe Or Data	(0)	(1)	(2)	(3)	(4)	(5)	(6)	(7)
C (Note 1)	В	Α	G (Note 2)	2Y0	2Y1	2Y2	2Y3	1Y0	1Y1	1Y2	1Y3
Х	Χ	Χ	Н	Н	Н	Н	Н	Н	Н	Н	Н
L	L	L	L	L	Н	Н	Н	Н	Н	Н	Н
L	L	Н	L	Н	L	Н	Н	Н	Н	Н	Н
L	Н	L	L	Н	Н	L	Н	Н	Н	Н	Н
L	Н	Н	L	Н	Н	Н	L	Н	Н	Н	Н
Н	L	L	L	Н	Н	Н	Н	L	Н	Н	Н
Н	L	Н	L	Н	Н	Н	Н	Н	L	Н	Н
Н	Н	L	L	Н	Н	Н	Н	Н	Н	L	Н
Н	Н	Н	L	Н	Н	Н	Н	Н	Н	Н	L

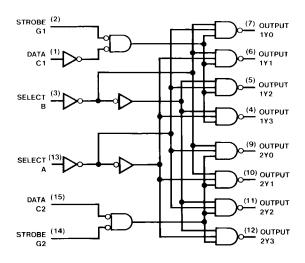
2-Line-to-4-Line Decoder or 1-Line-to-4-Line Demultiplexer

Inputs					Out	puts	
Sel	ect	Strobe	Data	1Y0	1Y1	1Y2	1Y3
В	Α	G1	C1	110		112	113
Х	Χ	Н	Х	Н	Н	Н	Н
L	L	L	Н	L	Н	Н	Н
L	Н	L	Н	Н	L	Н	Н
Н	L	L	Н	Н	Н	L	Н
Н	Н	L	Н	Н	Н	Н	L
Х	X	X	L	Н	Н	Н	Н

	Inputs				Out	puts	
Sel	ect	Strobe	Data	2Y0	2Y1	2Y1 2Y2	2Y3
В	Α	G2	C2	210	211	212	213
Х	Χ	Н	Х	Н	Н	Н	Н
L	L	L	L	L	Н	Н	Н
L	Н	L	L	Н	L	Н	Н
Н	L	L	L	Н	Н	L	Н
Н	Н	L	L	Н	Н	Н	L
Х	Χ	Х	Н	Н	Н	Н	Н

Note 2: G = inputs G1 and G2 connected together

Logic Diagram



H = HIGH level L = LOW level

X = don't care

Note 1: C = inputs C1 and C2 connected together

Absolute Maximum Ratings(Note 3)

Supply Voltage 7V Input Voltage 7V Operating Free Air Temperature Range 0°C to +70°C

Storage Temperature Range -65°C to +150°C

Note 3: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

DM74LS155 Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.75	5	5.25	V
V _{IH}	HIGH Level Input Voltage	2			V
V _{IL}	LOW Level Input Voltage			0.8	V
V _{OH}	HIGH Level Output Current			-0.4	mA
I _{OL}	LOW Level Output Current			8	mA
T _A	Free Air Operating Temperature	0		70	°C

DM74LS155 Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 4)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 \text{ mA}$			-1.5	V
V _{OH}	HIGH Level Output Voltage	V _{CC} = Min, I _{OH} = Max V _{IL} = Max, V _{IH} = Min	2.7	3.4		V
V _{OL}	LOW Level Output Voltage	$V_{CC} = Min, I_{OL} = Max$ $V_{IL} = Max, V_{IH} = Min$		0.35	0.5	V
		I _{OL} = 4 mA, V _{CC} = Min		0.25	0.4	
I	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 7V$			0.1	mA
I _{IH}	HIGH Level Input Current	$V_{CC} = Max, V_I = 2.7V$			20	μΑ
I _{IL}	LOW Level Input Current	$V_{CC} = Max, V_I = 0.4V$			-0.36	mA
Ios	Short Circuit Output Current	V _{CC} = Max (Note 5)	-20		-100	mA
I _{CC}	Supply Current	V _{CC} = Max (Note 6)		6.1	10	mA

Note 4: All typicals are at $V_{CC}=5V,\,T_A=25^{\circ}$ C.

Note 5: Not more than one output should be shorted at a time, and the duration should not exceed one second.

Note 6: I_{CC} is measured with all outputs OPEN, A,B, and C1 inputs at 4.5V, and C2, G1, and G2 inputs GROUNDED.

DM74LS155 Switching Characteristics

at $V_{CC} = 5V$ and $T_A = 25$ °C

	Parameter	From (Input)					
Symbol		To (Output)	C _L = 15 pF		C _L = 50 pF		Units
			Min	Max	Min	Max	
t _{PLH}	Propagation Delay Time	A, B, C2, G1		18		22	ns
	LOW-to-HIGH Level Output	or G2 to Y		16		22	ns
t _{PHL}	Propagation Delay Time	A, B, C2, G1		27		35	ns
	HIGH-to-LOW Level Output	or G2 to Y		21		33	115
t _{PLH}	Propagation Delay Time	A or B		18		24	ns
	LOW-to-HIGH Level Output	to Y		16		24	115
t _{PHL}	Propagation Delay Time	A or B		27	07	35	no
	HIGH-to-LOW Level Output	to Y		21	21		ns
t _{PLH}	Propagation Delay Time	C1		20	24	ns	
	LOW-to-HIGH Level Output	to Y		20		24	115
t _{PHL}	Propagation Delay Time	C1		27		35	ns
	HIGH-to-LOW Level Output	to Y		21		33	113

DM74LS156 Recommended Operating Conditions

Symbol	Parameter	Min	Nom	Max	Units
V _{CC}	Supply Voltage	4.75	5	5.25	V
V _{IH}	HIGH Level Input Voltage	2			V
V _{IL}	LOW Level Input Voltage			0.8	V
V _{OH}	HIGH Level Output Voltage			5.5	V
I _{OL}	LOW Level Output Current			8	mA
T _A	Free Air Operating Temperature	0		70	°C

DM74LS156 Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ (Note 7)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -18 \text{ mA}$			-1.5	V
I _{CEX}	HIGH Level Output Current	$V_{CC} = Min, V_O = 5.5V$ $V_{IL} = Max, V_{IH} = Min$			100	μА
V _{OL}	LOW Level Output Voltage	$V_{CC} = Min, I_{OL} = Max$ $V_{IL} = Max, V_{IH} = Min$		0.35	0.5	V
		$I_{OL} = 4 \text{ mA}, V_{CC} = Min$		0.25	0.4	
II	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 7V$			0.1	mA
I _{IH}	HIGH Level Input Current	$V_{CC} = Max, V_I = 2.7V$			20	μΑ
I _{IL}	LOW Level Input Current	$V_{CC} = Max, V_I = 0.4V$			-0.36	mA
I _{CC}	Supply Current	V _{CC} = Max (Note 8)		6.1	10	mA

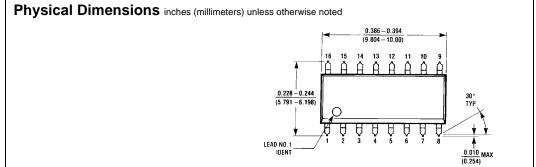
Note 7: All typicals are at $V_{CC} = 5V$, $T_A = 25^{\circ}$ C.

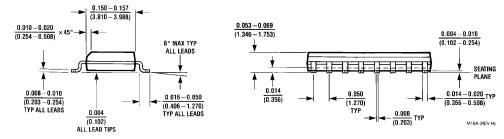
 $\textbf{Note 8:} \ \textbf{I}_{\texttt{CC}} \ \text{is measured with all outputs OPEN, A, B, and C1 inputs at 4.5V, and C2, G1, and G2 GROUNDED.}$

DM74LS156 Switching Characteristics

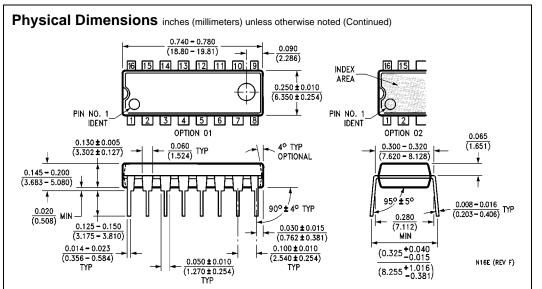
at $V_{CC} = 5V$ and $T_A = 25^{\circ}C$

		From (Input)					
Symbol	Parameter	To (Output)	C _L =	C _L = 15 pF		C _L = 50 pF	
			Min	Max	Min	Max	
t _{PLH}	Propagation Delay Time	A, B, C2, G1		28		53	ns
	LOW-to-HIGH Level Output	or G2 to Y		20		55	115
t _{PHL}	Propagation Delay Time	A, B, C2, G1		33		43	ns
	HIGH-to-LOW Level Output	or G2 to Y		33			115
t _{PLH}	Propagation Delay Time	A or B		28		53	ns
	LOW-to-HIGH Level Output	to Y		20		33	115
t _{PHL}	Propagation Delay Time	A or B		33		43	ns
	HIGH-to-LOW Level Output	to Y		33		43	ns
t _{PLH}	Propagation Delay Time	C1		28		53	ns
	LOW-to-HIGH Level Output	to Y		20		55	115
t _{PHL}	Propagation Delay Time	C1		34		43	ns
	HIGH-to-LOW Level Output	to Y		34		43	115





16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow Package Number M16A



16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N16E

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