Current Transducer LTSR 25-NP

For the electronic measurement of currents: DC, AC, pulsed, mixed, with galvanic isolation between the primary circuit (high power) and the secondary circuit (electronic circuit).









- 00	FIGO	data
		- CHICLE

\mathbf{I}_{PN}	Primary nominal current rms	25	At
I _{PM}	Primary current, measuring range	0 ± 80 ¹⁾	At
Î	Overload capability	250	At
V _{OUT}	Output voltage (Analog) @ I _P	$2.5 \pm (0.625$	I_p/I_{pN} V
	$I_p = 0$	2.5 2)	V
V_{REF}	Reference voltage (internal reference), Refour mode	2.5 ³⁾	V
	Reference voltage (external reference), Ref _{IN} mode	1.9 2.7 4)	V
G	Sensitivity	25	mV/A
N _s	Number of secondary turns (± 0.1 %)	2000	
R_{\perp}	Load resistance	≥ 2	$k\Omega$
CLmax	Maximum capacitive loading	500	рF
R_{IM}	Internal measuring resistance (± 0.5 %)	50	Ω
TCR	Temperature coefficient of R _{IM}	< 50	ppm/K
V _c	Supply voltage (± 5 %)	5	V
$I_{\rm c}$	Current consumption @ $V_c = 5 \text{ V}$ Typ	28+I _S ⁵⁾ +(V _o	$_{\rm UT}$ R / $_{\rm L}$) mA

Accuracy - Dynamic performance data

1.00	couracy - Dynamic performance u	ata		
Х	Accuracy @ I _{PN} , T _A = 25°C	U	± 0.2	%
	Accuracy @ I_{PN} , $I_A = 25^{\circ}C$ Accuracy with R_{IM} @ I_{PN} , $T_A = 25^{\circ}C$		± 0.7	%
$\epsilon_{\scriptscriptstyle \parallel}$	Linearity error		< 0.1	%
	CY		Max	
TCV	Temperature coefficient of $V_{OUT}/V_{RFF} @ I_{P} =$: 0		
	- 40°C	+ 85°C	37.5	ppm/K
TCG	Temperature coefficient of G - 40°C	+ 85°C	50 9	ppm/K
Vom	Magnetic offset voltage @ I₂ = 0,			
	after an overload of	3 x I _{PN}	± 0.5	mV
£ 100		5 x I _{PN}	± 2.0	mV
		10 x I _{PN}	± 2.0	mV
TCVRE	Temperature coefficient of internal V _{RFF}			
	@ $I_p = 0 - 10^{\circ}C$	+ 85°C	50	ppm/K
	- 40°C	- 10°C	100	ppm/K
t _{ra}	Reaction time @ 10 % of I _{PN}		< 100	ns
t,	Response time to 90 % of I _{PN} step		< 400	ns
di/dt	di/dt accurately followed		> 60	A/μs
BW	Frequency bandwidth (0 0.5 dB)		DC 100	kHz
	(0.5. 1.4D)		DC 200	니니ㅋ

(- 0.5 .. 1 dB) DC .. 200 kHz

Notes: 1) Only in ref_{OUT} mode or with external REF less than 2.525 V and greater than 2.475 V. For external REF out of these limits see leaflet. 2) V_{OUT} is linked to V_{REF}, by conception the difference between these two nodes for I_P = 0 is maximum ± 25 mV, 2.475 V < V_{OUT} < 2.525 V. 3) In Ref_{OUT} mode at T_A = 25°C, 2.475 V < V_{REF} < 2.525 V. The minimal impedance loading the ref pin should be > 220 kΩ. Internal impedance = 600 Ω. For most applications you need to buffer this output to feed it into an ADC for example. 4) To overdrive the REF (1.9 V .. 2.7 V) max ± 1 mA is needed. 5) I_S = I_P/N_S, 6) Only due to TCR I_M.

$I_{DN} = 25 \text{ At}$

Features

- Closed loop (compensated) multirange current transducer using the Hall effect
- Unipolar voltage supply
- Isolated plastic case recognized according to UL 94-V0
- Compact design for PCB mounting
- Incorporated measuring resistance
- · Extended measuring range
- Access to the internal voltage reference
- Possibility to feed the transducer reference from external supply.

Advantages

- Excellent accuracy
- Very good linearity
- Very low temperature drift
- · Optimized response time
- Wide frequency bandwidth
- No insertion losses
- High immunity to external interference
- Current overload capability.

Applications

- AC variable speed drives and servo motor drives
- Static converters for DC motor drives
- · Battery supplied applications
- Uninterruptible Power Supplies (UPS)
- Switched Mode Power Supplies (SMPS)
- Power supplies for welding applications.

Application Domain

Industrial.

Current Transducer LTSR 25-NP

(General data				
$T_{\scriptscriptstyle \Delta}$	Ambient operating temperature	- 40 + 85	°C		
Ts	Ambient storage temperature	- 40 + 100	°C		
m	Mass	10	g		
	Standards 1)	EN 50178: 199	7		
		IEC 60950-1: 2	001		

Iso	olation characteristic					
V_{d}	Rms voltage for AC isolation test, 50 Hz, 1 min	3	kV			
v _w	Impulse withstand voltage 1.2/50 μs	> 8	kV			
		Min				
$V_{\rm e}$	Rms voltage for partial discharge extinction 10 pC	> 1.5	kV	A-N		
		Min				
d Cp	Creepage distance 1)	15.35	mm	Olu.		
d Cl	Clearance distance 2)	6.2	mm	C		
CTI	Comparative Tracking Index (group IIIa)	175		- DUI		
Appli	ications examples		-10	op.cpu.com.tw		
According to EN 50178 and CEI 61010-1 standards and following conditions:						
 Over voltage category OV 3 Pollution degree PD2 Non-uniform field 						
_	EN 50178	EIC 610	10-1			

Applications examples

- Over voltage category OV 3
- Pollution degree PD2
- Non-uniform field

	EN 50178	EIC 61010-1	
dCp, dCl, $\hat{m{V}}_{_{\!\!W}}$	Rated insulation voltage	Nominal voltage	
Single insulation	600 V	600 V	
Reinforced insulation	300 V	300 V	

Notes: 1) On housing

²⁾On PCB with soldering pattern UTEC93-703.



This transducer must be used in electric/electronic equipment with respect to applicable standards and safety requirements in accordance with the manufacturer's operating instructions.



Caution, risk of electrical shock

When operating the transducer, certain parts of the module can carry hazardous voltage (eg. primary busbar, power supply).

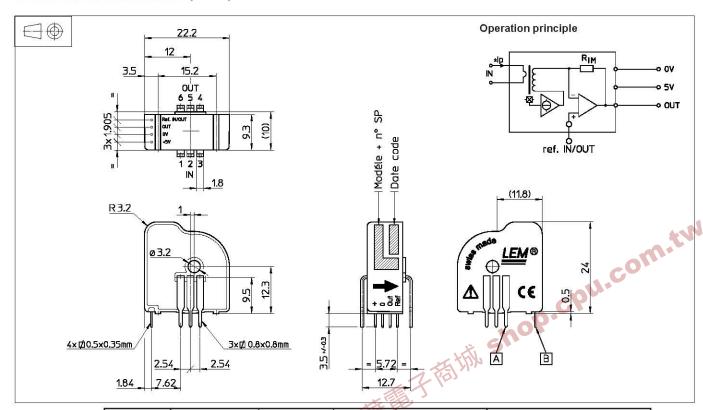
Ignoring this warning can lead to injury and/or cause serious damage.

This transducer is a built-in device, whose conducting parts must be inaccessible after installation.

A protective housing or additional shield could be used.

Main supply must be able to be disconnected.

Dimensions LTSR 25-NP (in mm.)



	Number of primary turns	Primary nominal current rms I _{PN} [A]	Nominal ¹⁾ output voltage V out [V]	Primary resistance \mathbf{R}_p [$m\Omega$]	Primary insertion inductance L , [µH]	Recommended connections
	1	± 25	2,5 ± 0.625	0.18	0.013	6 5 4 OUT O-O-O-O IN 1 2 3
	2	± 12	2.5 ± 0.600	0.81	0.05	6 5 4 OUT 0 0 0 IN 1 2 3
7	53	±8	2.5 ± 0.600	1.62	0.12	6 5 4 OUT

Mechanical characteristics

General tolerance

 Fastening & connection of primary Recommended PCB hole

 Fastening & connection of secondary Recommended PCB hole

· Additional primary through-hole

± 0.2 mm

6 pins 0.8 x 0.8 mm

1.3 mm

4 pins 0.5 x 0.35 mm

0.8 mm

 \varnothing 3.2 mm

Remarks

- V_{OUT} swings above the 2.5 V offset when I_p flows from terminals 1, 2, 3 to terminals 4, 5, 6 (with the arrow)
- For the EMC, the acceptance criteria are available on request
- Temperature of the primary conductor should not exceed
 100°C

Note: 1) Output voltage when LTSR 25-NP is used with internal reference.

Output Voltage - Primary Current

